

## Abstract

A data reception method is described in which errors due to a phase shift between the data signals and the clock pulse are compensated in data signals transmitted at a constant frequency with the receiving device operating at this frequency by operating two readout devices with a time offset, and a signal output by one of the readout devices is selected, after which this signal is checked for validity. This makes additional transmission of an operating clock pulse via a bus system and synchronization of an input signal with an operating clock unnecessary.

(Figure 1)